



International Workshop on System-Level Interconnect Prediction

April 1-2, 2005

San Francisco, CA

Chair's Welcome





Technical Program

- Saturday all day & Sunday morning
- 14 presentations in 8 sessions
 - 4 invited talks (Sun, TI, IBM, Dresden)
- Highlights
 - *Technology scaling*
 - *Manufacturing variability*
 - *The effect of supply noise on timing*
 - *Current density stress & electro-migration*
 - CMOS-compatible optical interconnects
 - Reconfigurable interconnects



Meals & Social Program

- Breakfast included in the hotel rate
 - Served 7am-12noon
- Brunch at 9:50am
- Lunch at 12noon
 - Served in the Heritage room
- Snacks in the afternoon (Saturday only)
- Dinner on your own
- Trip to Alcatraz on Sunday
 - Leaving the hotel at 1pm, back by 5:30pm



Trip to Alcatraz

Tour schedule

- Leave Hotel @ 1:00pm
- Ride Cable Car
- Arrive at Pier 41 @ 1:45pm
- Depart from Pier 41 to Alcatraz @ 2:15pm
- Return from Alcatraz to Pier 41 @ 4:30pm



Organizers of SLIP 2005

- Igor Markov, *U. of Michigan*
General Chair
- Mike Hutton, *Altera*
Technical Program Chair
- Amir Farrahi, *Sun Microsystems*
Special Sessions Chair
- Payman Zarkesh-Ha, *LSI Logic*
Local Arrangements Chair
- Joni Dambre, *Ghent Univ., Belgium*
Publications Chair
- Lou Scheffer, *Cadence*
Finance Chair



Session Chairs

- 1 – Lou Scheffer, Cadence
- 2 – **Igor Markov**, Michigan
- 3 – Mike Hutton, Altera
- 4 – Dirk Stroobant, Ghent Univ.
- 5 – Amir Farrahi, Sun
- 6 – Payman Zarkesh-Ha, LSI Logic
- 7 – **Joni Dambre**, Ghent Univ.
- 8 - Igor Markov, Michigan



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